

# OXY5361A

3.5" SBC with Intel<sup>®</sup> Skylake-U SoC



# **Safety Information**

#### **Electrical safety**

- To prevent electrical shock hazard, disconnect the power cable from the electrical outlet before relocating the system.
- When adding or removing devices to or from the system, ensure that the power cables for the devices are unplugged before the signal cables are connected. If possible, disconnect all power cables from the existing system before you add a device.
- Before connecting or removing signal cables from the motherboard, ensure that all power cables are unplugged.
- Seek professional assistance before using an adapter or extension cord. These devices could interrupt the grounding circuit.
- Make sure that your power supply is set to the correct voltage in your area.
- If you are not sure about the voltage of the electrical outlet you are using, contact your local power company.
- If the power supply is broken, do not try to fix it by yourself. Contact a qualified service technician or your local distributor.

#### **Operation safety**

- Before installing the motherboard and adding devices on it, carefully read all the manuals that came with the package.
- Before using the product, make sure all cables are correctly connected and the power cables are not damaged. If you detect any damage, contact your dealer immediately.
- To avoid short circuits, keep paper clips, screws, and staples away from connectors, slots, sockets and circuitry.
- Avoid dust, humidity, and temperature extremes. Do not place the product in any area where it may become wet.
- Place the product on a stable surface.
- If you encounter any technical problems with the product, contact your local distributor

#### Statement

- All rights reserved. No part of this publication may be reproduced in any form or by any means, without prior written permission from the publisher.
- All trademarks are the properties of the respective owners.
- All product specifications are subject to change without prior notice



# **Revision History**

Revision	Date (dd.mm.yyyy)	Changes		
Version 0.9	01.06.2017	Draft		
Version 1.0	13.06.2017	Initial Release		
Version 1.1	15.06.2017	Update latest product photo		
		Change JP13.JP14,JP15 default setting Change		
		LVDS and CN18 pin define.		
Version1.2	02.12.2020	Add LVDS VDD JP24 pin define		

# **Packing list**

- OXY5361A 3.5" SBC
- CD (Driver + user's manual)
- Optional Accessories
  - O Cable kit
    - SATA cable
    - SATA power cable
    - USB cable
    - COM cable
  - O Thermal kit:
    - Copper heat spreader
    - Passive heatsink



If any of the above items is damaged or missing, please contact your local distributor.



# **Table of Contents**

Safety Information	
Electrical safety	
Operation safety	
Charles and	1
Statement	1
Revision History	2
Packing list	2
Chapter 1: Product Information	5
1.1 Block Diagram	5
1.2 Key Features	
1.3 Board Placement	7
Chapter 2: Jumpers and Connectors	
2.1 Onboard connector and jumper list	
2.2 Connector and jumper nin definition	٥
2.2 Connector and jumper pin dejinition	وع ۵
IP1: Clear CMOS	رع م
JP2: COM1 RI/+5V/+12V	9
JP13: AT/ATX Mode	9
JP14: mPCle1 signal select	
JP15: mPCle2 signal select	
SW1: LVDS Resolution	
2.2.2 Connector	
LVDS	
CN18: LVDS Control connector	
JP24 : LVDS VDD connector	
J1: DIO	
J9: Front Panel	
J10: LPC	
J11: Audio	
J12: COM2 RS232/422/485	
J14: USB2.0 x2	
Chapter 3: AMI BIOS UTILITY	
3.1 Starting	
3.2 Navigation Keys	
3.3 Main Menu	
3.4 Advanced Menu	
3.4.1 Trust Computing	
3.4.2 IT8786 Super IO Configuration	
3.4.2.1 Serial Port 1 Configuration	
3.4.2.2 Serial Port 2 Configuration	19
3.4.3 Hardware Monitor	20
3.4.4 CPU Configuration	20
3.4.5 SATA Configuration	21
3.4.6 CMS Configuration	
3.4.7 USB Configuration	

3.5 Chipset	23
3.5.1 System Agent (SA) Configuration	23
3.5.1.1 Graphic Configuration	24
3.5.1.1.1 LCD Control	24
3.5.1.2 Memory Configuration	25
3.5.2 PCH-IO Configuration	25
3.6 Security	26
3.7 Boot Menu	27
3.8 Save & Exit	27

# **Chapter 1: Product Information**

#### 1.1 Block Diagram



# 1.2 Key Features

Processor & System			
СРИ Туре	Intel <sup>®</sup> Skylake-U series, BGA type		
	Intel® Core™ i3-6100U (3M Cache, 2.3 GHz) (15W)		
	Intel® Core™ i5-6300U (3M Cache, 2.4 GHz) (15W)		
	Intel® Core™ i7-6600U (4M Cache, 2.6 GHz) (15W)		
Memory Type	2 x SO-DIMM DDR4 2133 MHz up to 32 GB		
BIOS	AMI <sup>®</sup> UEFI BIOS		
Super I/O	Fintek F8786E		
Watchdog	1-255 sec.		
Expansion Slot	1 x Full-size mPCIe/mSATA		
	1 x Half-size mPCle/mSATA		
Display			
Chipset	Intel <sup>®</sup> Integrated		
DisplayPort	Max. resolution 4096 x 2304		
LVDS	Dual channel 24-bit LVDS		
Display Type	DisplayPort, LVDS		
Audio			
Codec	Realtek ALC888 High Definition Audio Codec		
Ethernet			
Chipset	Intel <sup>®</sup> I210-IT + I219-LM		
WOL	Yes		
Boot from LAN	Yes for PXE		
Rear I/O			
DisplayPort	2 x DisplayPort		
Ethernet	2 x RJ45		
USB	4 x USB 3.0		
СОМ	1 x RS232		
Internal I/O			
SATA	1 x SATAIII (6 Gb/s)		
USB	2 x USB 2.0		
СОМ	1 x RS232/422/485		
Audio	Mic-in, Line-In/out		
FAN	1 x CPU fan		
LVDS	30-pin connector		
DIO	8-bit		
Mechanical and Enviror	iment		
Form Factor	3.5" SBC		
Power Type	9~36V DC-in, 4-pin ATX power connector, AT/ATX mode support		
Dimension	146 x 101mm (5.7" x 4")		
Operating Temp.	-40 to 85°C		
Storage Temp	-40 to 85°C		
Relative Humidity	10% to 90%, non-condensing		

\*All specifications and photos are subject to change without notice.

#### **1.3 Board Placement**





MPCIE2

Skylake-U Processor



# **Chapter 2: Jumpers and Connectors**

Label	Function	
COM1 (Rear)	RS232, DB9 connector	
DP1 (Rear)	DisplayPort	
DP2 (Rear)	DisplayPort	
LAN1 (Rear)	RJ45 connector	
LAN2 (Rear)	RJ45 connector	
USB1/2 (Rear)	2 x USB3.0	
USB3/4 (Rear)	2 x USB3.0	
MPCIE1	Full-size mPCIe/mSATA	
MPCIE2	Half-size mPCle/mSATA	
AMP1	Audio amplifier	
LVDS	LVDS	
CN18	LVDS Control Connector	
J1	DIO header	
15	SATAIII connector	
J6	SATA power	
19	Front panel	
J10	LPC header	
J11	Audio	
J12	COM2, RS232/422/485	
J13	CPU fan	
J14	2 x USB2.0, USB5/6	
SW1	LVDS resolution Select	
JP1	Clear CMOS	
JP2	COM1 RI/+5V/+12V select	
JP13	AT/ATX select	
JP14	mPCIE1 SATA/PCIe select	
JP15	mPCIE2 SATA/PCIe select	

#### 2.1 Onboard connector and jumper list

# 2.2 Connector and jumper pin definition 2.2.1 Jumper

#### JP1: Clear CMOS

Jumper	Function description	Setting	
1-2	Normal		
2-3	Clear CMOS		
Default setting: 1-2			

## JP2: COM1 RI/+5V/+12V

Jumper	Function description	Setting	
1-2	Ring	1 2	
		3 🔾 4	
		5 0 6	
3-4	5V	1 2	
		3 0 4	
		5 0 6	
5-6	12V	1 2	
		3 0 4	
		5 0 6	
Default setting: 1-2			

# JP13: AT/ATX Mode

Jumper	Function description	Setting
1-2	ATX	
		2 3
2-3	AT	1
		2
		3
Default s	setting: 2-3	

# JP14: mPCle1 signal select

Jumper	Function description Setting		
1-2	SATA	1	
		2	
		3	
2-3	PCle	1	
		2	
		3	
Default setting: 2-3			



# JP15: mPCle2 signal select

Jumper	Function description Setting			
1-2	SATA	1		
		2		
		3		
2-3	PCle	1		
		2		
		3		
Default s	Default setting: 2-3			

## SW1: LVDS Resolution

	Color			Set	ting		
Resolution	depth	Channel	Switch 1	Switch 2	Switch 3	Switch 4	Drawing
800 x 600	18-bit	Single	Off	Off	Off	Off	SW1
1024 x 768	18-bit	Single	On	Off	Off	Off	
1024 x 768	24-bit	Single	Off	On	Off	Off	2
1280 x 800	18-bit	Single	On	On	Off	Off	$\frac{3}{4}$
1280 x 1024	24-bit	Dual	Off	Off	On	Off	
1366 x 768	24-bit	Single	On	Off	On	Off	
1440 x 900	24-bit	Dual	Off	On	On	Off	
1920 x 1080	24-bit	Dual	On	On	On	Off	



## 2.2.2 Connector

LVDS
------

	<u> </u>		
Pin	Definition	Pin	Definition
1	LVDS_CLK+	2	GND
3	LVDS_CLK-	4	LVDSA3+
5	GND	6	LVDSA3-
7	LVDSB3+	8	GND
9	LVDSB3-	10	LVDSA_CLK+
11	LVDSB2+	12	LVDSA_CLK-
13	LVDSB2-	14	GND
15	LVDSB1+	16	LVDSA2+
17	LVDSB1-	18	LVDSA2-
19	LVDSB0+	20	LVDSA1+
21	LVDSB0-	22	LVDSA1-
23	GND	24	LVDSA0+
25	LVDS_SCLK	26	LVDSA0-
27	LVDS_SDATA	28	GND
29	VDD	30	VDD



# CN18: LVDS Control connector

PIN	DEFINITION		
1	+12V	1	ᇉᅮ
2	+12V	-	
3	+12V		
4	5V		
5	5V		
6	GND		
7	GND		
8	Backlight Enable		
9	Backlight control		
10	GND		
11	JP24 : LVDS VDD	10	
	connector		لمط

JP24	$\Gamma_l$	VDS	Signal	Power
1-	2		5V	
2-	3		3V3	

#### J1: DIO

PIN	DEFINITION	ſ	न्न
1	DIO0	1	
2	DIO1		
3	DIO2		
4	DIO3		
5	DIO4		
6	DIO5		
7	DIO6		
8	DIO7		
9	3.3V	10	
10	GND		٢



#### J9: Front Panel

PIN	DEFINITION	PIN	DEFINITION			,
1	FP_HDLED+	2	FP_PLED+	1		2
3	SATALED#	4	GND			
5	GND	6	EC_PWR_BTN		00	
7	RESET-	8	GND	9	Õ	10
9						,

# J10: LPC

PI N	DEFINITION		┏᠊᠊᠊┫
1	GND	1	
2	TPM_IRQ		
3	+3.3VSB		
4	LPC_AD0		
5	LPC_AD1		
6	LPC_AD2		
7	LPC_AD3		
8	LPC_FRAME#	10	
9	PLTRST#_BUF1	10	E-
10	LPC_CLK_1	, I	

#### J11: Audio

PIN	DEFINITION	1	Ē
1	GND	1	
2	MIC1_JD		
3	MIC1_R		
4	MIC1_L		
5	FRONT_JD		
6	FRONT_R		
7	FRONT_L		
8	LINE1_JD		
9	LINE1_R	10	
10	LINE1_L		٢

# J12: COM2 RS232/422/485

## Note: RS232/422/485 can be set by BIOS.

Pin	RS232 mode (Default)	RS422 mode	RS485 mode	1
1	5V			हिन्दी
2	GND	GND	GND	1 🗖
3	RI			
4	DTR	RX-		
5	CTS			
6	TXD	RX+		
7	RTS			
8	RXD	TX+	DATA-	
9	DSR			
10	DCD	TX-	DATA+	

## J14: USB2.0 x2

PIN	DEFINITION	Γ	Ц
1	+5V	1	
2	USB5-		
3	USB5+		
4	GND		
5	GND		
6	+5V		
7	USB6-		
8	USB6+		
9	GND	10	
10	GND		٢٩



# **Chapter 3: AMI BIOS UTILITY**

This chapter provides users with detailed descriptions on how to set up a basic system configuration through the AMI BIOS setup utility.

#### 3.1 Starting

To enter the setup screens, perform the following steps:

- Turn on the computer and press the <Del> key immediately.
- After the <Del> key is pressed, the main BIOS setup menu displays. Other setup screens can be accessed from the main BIOS setup menu, such as the Chipset and Power menus.

#### 3.2 Navigation Keys

The BIOS setup/utility uses a key-based navigation system called hot keys. Most of the BIOS setup utility hot keys can be used at any time during the setup navigation process.

Some of the hot keys are <F1>, <F10>, <Enter>, <ESC>, and <Arrow> keys.



Some of the navigation keys may differ from one screen to another.

Left/Right	The Left and Right <arrow> keys moves the cursor to select a menu.</arrow>	
Up/Down	The Up and Down <arrow> keys moves the cursor to select a setup</arrow>	
	screen or sub-screen.	
+– Plus/Minus	The Plus and Minus < Arrow> keys changes the field value of a	
	particular setup setting.	
F1	The <f1> key offer general help.</f1>	
F2	The <f3> key load previous values.</f3>	
F3	The <f3> key load optimized defaults.</f3>	
F4	The <f4> key saves any changes made and exits the BIOS setup</f4>	
	utility.	
Esc	The <esc> key discards any changes made and exits the BIOS setup</esc>	
	utility.	
Enter	The <enter> key displays a sub-screen or changes a selected or</enter>	
	highlighted option in each menu.	



## 3.3 Main Menu

The Main menu is the first screen that you will see when you enter the BIOS Setup Utility.

BIOS Information BIOS Vendor Americ Project Version DXY533 Build Date and Time 05/05/ Access Level Admin: System Date [Thu 0 System Time [12:29	An Megatrends 1 0.02 x64 2017 11:09:59 strator 6/01/2017] :26]
	<pre> ++: Select Screen  f↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>

#### 3.4 Advanced Menu

The Advanced Menu allows you to configure your system for basic operation. Some entries are defaults required by the system board, while others, if enabled, will improve the performance of your system or let you set some features according to your preference. <u>Setting incorrect field values may cause the</u>





#### **3.4.1 Trust Computing**



# 3.4.2 IT8786 Super IO Configuration

Aptio Setup Utility Advanced	– Copyright (C) 2017 Americar	n Megatrends, Inc.
IT8786 Super IO Configuration		Set Parameters of Serial Port
Super IO Chip ▶ Serial Port 1 Configuration ▶ Serial Port 2 Configuration	IT8786	1 (CUMA)
		++: Select Screen fl: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Version 2.18.1259.	Copyright (C) 2017American M	legatrends, Inc.

# 3.4.2.1 Serial Port 1 Configuration

Aptio Setup Utility - Advanced	Copyright (C) 2017 Americar	n Megatrends, Inc.
Serial Port 1 Configuration		Enable or Disable Serial Port
Serial Port Device Settings	[Enabled] IO=3F8h; IRQ=4;	(604)
Change Settings	[Auto]	
		→+: Select Screen ↑↓: Select Item
		Enter: Select +/-: Change Opt.
		F1: General Help F2: Previous Values
		F3: Optimized Detaults F4: Save & Exit ESC: Exit
Version 2.18.1259. 0	opyright (C) 2017American M	legatrends, Inc.



#### 3.4.2.2 Serial Port 2 Configuration

Aptio Setup Utility - Advanced	Copyright (C) 2017 American	Megatrends, Inc.
Serial Port 2 Configuration Serial Port Device Settings	[Enabled] IO=2F8h; IRQ=3;	Enable or Disable Serial Port (COM)
Change Settings Device Mode Device Termination	[Auto] [RS422] [Disabled]	
		<pre>↑↓: Select Item Enter: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>
Version 2.18.1259. Co	pyright (C) 2017 American M	egatrends, Inc.





## 3.4.3 Hardware Monitor

Aptio Setup Ut Advanced	ility – Copyright (C) 2017 Ameri	ican Megatrends, Inc.
Pc Health Status		
CPU temperature Fan Speed VCORE VDDQ +12V +5V VSB3V VBAT	: +47 C : N/A : +0.960 V : +1.200 V : +11.808 V : +5.160 V : +3.312 V : +3.048 V	<pre>**: Select Screen fl: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>
Version 2.18.	1259. Copyright (C) 2017 America	an Megatrends, Inc.

# 3.4.4 CPU Configuration

Aptio Setup Utilit Advanced	y – Copyright (C) 2017 Amer	rican Megatrends, Inc.
CPU Configuration Intel(R) Core(TM) i7-6600U CPU © CPU Signature CPU Speed Processor Cores Hyper Threading Technology L1 Data Cache L1 Code Cache L2 Cache L3 Cache	2.60GHz 406E3 2600 MHz 2 Supported 32 kB × 2 32 kB × 2 256 kB × 2 4 MB	Enabled for Windows XP and Linux (OS optimized for Hyper-Threading Technology) and Disabled for other OS (OS not optimized for Hyper-Threading Technology). When Disabled only one thread per enabled core is enabled.
Hyper-threading Active Processor Cores	[Enabled] [A11]	<pre>++: Select Screen fl: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>
Version 2.18.1259	. Copyright (C) 2017 Americ	can Megatrends, Inc.



#### 3.4.5 SATA Configuration



Aptio S Advanced	etup Utility – Copyright (C) 20	17 American Megatrends, Inc.
SATA Mode Selection	[AHCI]	Determines how SATA controller(s) operate.
Port 0 Serial ATA Port 1	[Enabled] Empty	
Port 1 Serial ATA Port 2 Port 2	[Enabled] Empty [Enabled]	
	SATA Mode Selec AHCI RAID	tion ++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Versio	n 2.18.1259. Copyright (C) 2017	American Megatrends, Inc.



# 3.4.6 CMS Configuration

Aptio Setup Utility - Advanced	Copyright (C) 2017 American	Megatrends, Inc.
Compatibility Support Module Configu	ration	Enable/Disable CSM Support.
CSM Support	[Enabled]	
CSM16 Module Version	07.79	
Boot option filter Option ROM execution	[UEFI and Legacy]	
Network Storage Video Other PCI devices	[Do not launch] [Legacy] [Legacy] [UEFI]	<pre> ++: Select Screen fl: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>
Version 2.18.1259. Co	pyright (C) 2017 American M	egatrends, Inc.

#### 3.4.7 USB Configuration

Aptio Setu Advanced	up Utility – Copyright (C) 201	7 American Megatrends, Inc.
USB Configuration		Enables Legacy USB support.
USB Devices: 1 Keyboard		support if no USB devices are connected. DISABLE option will
Legacy USB Support XHCI Hand—off	[Enabled] [Enabled]	keep USB devices available only for EFI applications.
		++: Select Screen
		Enter: Select +/-: Change Opt.
		F1: General Help F2: Previous Values F3: Optimized Defaults
		F4: Save & Exit ESC: Exit
Vencion	2 19 1259 Conunidat (C) 2017 (	American Merataende Inc



-

#### 3.5 Chipset



#### 3.5.1 System Agent (SA) Configuration

Chipset	otilitg - copyright (c) 2017 Hilleri	Loan Megatrends, Inc.
System Agent Bridge Name	Skylake	Graphics Configuration
<ul> <li>▶ Graphics Configuration</li> <li>▶ Memory Configuration</li> </ul>		++: Select Screen †↓: Select Item Enter: Select +/-: Change Opt. F1: General Help
		F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit



# 3.5.1.1 Graphic Configuration

Aptio Setup Utility – Copyright (C) 2017 American Chipset	Megatrends, Inc.
Graphics Configuration	LCD Control
▶ LCD Control	
	++: Select Screen
	Enter: Select Item
	F1: General Help F2: Previous Values
	F3: Optimized Defaults F4: Save & Exit
	ESC: Exit
version 2.18.1259. Copyright (C) 2017 American M	egatrends, Inc.

#### 3.5.1.1.1 LCD Control

Aptio Setup Utility - Chipset	- Copyright (C) 2017 Americar	n Megatrends, Inc.
LCD Control Primary IGFX Boot Display Secondary IGFX Boot Display Active LFP	[DP1] [DP2] [No LVDS]	Select the Video Device which will be activated during POST. This has no effect if external graphics present. Secondary boot display selection will appear based on your selection. VGA modes will be supported only on primary display
		<pre>++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>
Version 2.18.1259. (	Copyright (C) 2017 American M	legatrends, Inc.



# 3.5.1.2 Memory Configuration

	Aptio Setup Utility – Chipset	Copyright (C) 2017 American	Megatrends, Inc.
Memory Config	guration		
Total Memory		16384 MB	
DIMM#1 DIMM#2		16384 MB Not Present	
			<pre>→+: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>
	Version 2.18.1259. Co	opyright (C) 2017 American M	egatrends, Inc.

# 3.5.2 PCH-IO Configuration

Aptio Setup Util Chipset	ity – Copyright (C) 2017 Americ	can Megatrends, Inc.
Intel PCH RC Version Intel PCH SKU Name	2.0.0.0 PCH-LP Mobile (U) Premium SKU	
Intel PCH Rev ID	21/C1	
		++: Select Screen †J: Select Item
		Frier: Select +/-: Change Opt. F1: General Help E2: Previous Values
		F3: Optimized Defaults F4: Save & Exit F5C: Fyit
Version 2, 18, 12	59 Conuright (C) 2017 American	Megatrends Inc



# 3.6 Security

Aptio Setup Utility – Copyright (C) 2017 American Megatrends, Inc. Main Advanced Chipset <mark>Security</mark> Boot Save & Exit		
Password Description If ONLY the Administrator's passw then this only limits access to St only asked for when entering Setu If ONLY the User's password is se is a power on password and must be boot or enter Setup. In Setup the have Administrator rights. The password length must be in the following range:	ord is set, etup and is o. t, then this e entered to User will	Set Administrator Password
Minimum length Maximum length Administrator Password User Password	3 20	<pre>++: Select Screen f1: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>
Version 2.18.1259. Copyright (C) 2017 American Megatrends, Inc.		

**Administrator Password** Set administrator password.

**User Password** Set user Password.



#### 3.7 Boot Menu

Aptio Setup Utility – Copyright (C) 2017 American Megatrends, Inc. Main Advanced Chipset Security <mark>Boot</mark> Save & Exit		
Boot Option Priorities Driver Option Priorities	<pre>++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>	
Version 2.18.1259. Copyright (C) 2017 American Megatrends, Inc.		

This section is used to configure the boot features.

#### 3.8 Save & Exit

Aptio Setup Utility – Copyright (C) 2017 American Main Advanced Chipset Security Boot Save & Exit	Megatrends, Inc.	
Save Options Save Changes and Exit Discard Changes and Exit	Exit system setup after saving the changes.	
Default Options Restore Defaults		
Boot Override Launch EFI Shell from filesystem device		
	<pre>→+: Select Screen f↓: Select Item Enter: Select</pre>	
	+/-: Change Opt. F1: General Help F2: Previous Values	
	F3: Optimized betauits F4: Save & Exit ESC: Exit	
Version 2.18.1259. Copyright (C) 2017 American Megatrends, Inc.		

